AMENDMENTS TO THE CLAIMS

Please add or amend the claims to read as follows, and cancel without prejudice or disclaimer to resubmission in a divisional or continuation application claims indicated as cancelled:

1.-12. (Cancelled)

13. (Original) A method of verifying a design, comprising the steps of:

generating a stream of instructions, said stream including a first instruction that references an unidentified first resource;

setting a first flag that designates said first resource as being unidentified;

copying said first resource to a second resource;

setting a second flag that designates said second resource as being unidentified;

including said second resource in a set of unidentified resources that is associated with said first resource, wherein cach member of said set has a status flag designating said member as being unidentified; and

thereafter performing the steps of

identifying said second resource;

clearing said second flag; and

removing said second resource from said set.

14. (Original) The method according to claim 13, wherein said step of identifying said second resource further comprises the steps of:

identifying each said member of said set with said second resource; and

clearing said status flag of each said member.

15. (Original) Thc method according to claim 14, further comprising the step of accessing said second resource.

16. (Original) The method according to claim 13, further comprising the step of accessing said second resource.

17. (Original) The method according to claim 13, wherein said step of identifying said second resource comprises requesting a content of said second resource.

18. (Original) A computer software product, comprising a computer-readable medium in which computer program instructions are stored, which instructions, when read by a computer, cause the computer to perform a method of verifying a design, comprising the steps of:

generating a stream of design instructions, said stream including a first instruction that references an unidentified first resource;

setting a first flag that designates said first resource as being unidentified;

copying said first resource to a second resource;

setting a second flag that designates said second resource as being unidentified;

including said second resource in a set of unidentified resources that is associated with said first resource, wherein each member of said set has a status flag designating said member as being unidentified; and

thereafter performing the steps of

identifying said second resource;

clearing said second flag; and

removing said second resource from said set.

19. (Original) The computer software product according to claim 18, wherein said step of identifying said second resource further comprises the steps of:

identifying each said member of said set with said second resource; and

clearing said status flag of each said member.

20. (Original) The computer software product according to claim 19, further comprising the step of accessing said second resource.

21. (Original) The computer software product according to claim 18, further comprising the step of accessing said second resource.

22. (Original) The computer software product according to claim 18, wherein said step of identifying said second resource comprises requesting a content of said second resource.

23. (Original) A verification system for verifying a design, comprising a test generator adapted to perform the steps of:

generating a stream of instructions, said stream including a first instruction that references an unidentified first resource;

setting a first flag that designates said first resource as being unidentified;

copying said first resource to a second resource;

setting a second flag that designates said second resource as being unidentified;

including said second resource in a set of unidentified resources that is associated with said first resource, wherein each member of said set has a status flag designating said member as being unidentified; and

thereafter performing the steps of:

identifying said second resource;

clearing said second flag; and

removing said second resource from said set.

24. (Original) The verification system according to claim 23, wherein in said step of identifying said second resource said test generator is further adapted to identify each said member of said set with said second resource; and to clear said status flag of each said member.

25. (Original) The verification system according to claim 23, wherein immediately prior to identifying said second resource said test generator requests a content of said second resource.

26. (Original) A method of verifying a design, comprising the steps of:

generating a stream of instructions for evaluation of a Boolean function in said design;

constructing a set of inputs for said Boolean function, said set comprising members having unidentified input resources, and said inputs being outcome determinative of said Boolean function, said Boolean function further having an output resource;

selecting one of said members;

resolving an identity of said selected member;

excluding said selected member from said set of inputs;

removing all remaining members of said set of inputs that are no longer outcome determinative of said Boolean function;

iterating said steps of selecting, resolving, excluding and removing until no more than one member remains in said set of inputs; and

determining said output resource as a copy of said one member.

27. (Original) The method according to claim 26, further comprising the steps of:

setting a flag that designates said output resource as being unidentified; and

including said output resource in a set of unidentified resources that is associated with said one member, respective status flags being associated with each member of said set of unidentified resources that designate an unidentified status thereof.

28. (Original) The method according to claim 27, further comprising the steps of:

associating a respective inversion flag with each of said input resources and said output resource that indicate an inversion status thereof; and

setting said inversion flag of said output resource to a negation of said inversion flag of one of said input resources that corresponds to said one member.

29. (Original) A computer software product, comprising a computer-readable medium in which computer program instructions are stored, which instructions, when read by a computer, cause the computer to perform a method of verifying a design, comprising the steps of:

generating a stream of design instructions for evaluation of a Boolean function in said design;

constructing a set of inputs for said Boolean function, said set comprising members having unidentified input resources, and said inputs being outcome determinative of said Boolean function, said Boolean function further having an output resource;

selecting one of said members;

resolving an identity of said selected member;

excluding said selected member from said set of inputs;

removing all remaining members of said set of inputs that are no longer outcome determinative of said Boolean function;

iterating said steps of selecting, resolving, excluding and removing until no more than one member remains in said set of inputs; and

determining said output resource as a copy of said one member.

30. (Original) The computer software product according to claim 29, wherein said computer is further instructed to perform the steps of:

setting a flag that designates said output resource as being unidentified; and

including said output resource in a set of unidentified resources that is associated with said one member, respective status flags being associated with each member of said set of unidentified resources that designate an unidentified status thereof.

31. (Original) The computer software product according to claim 30, wherein said computer is further instructed to perform the steps of:

associating a respective inversion flag with each of said input resources and said output resource that indicate an inversion status thereof; and

setting said inversion flag of said output resource to a negation of said inversion flag of one of said input resources that corresponds to said one member.

32. (Original) A verification system of verifying a design, comprising a test generator adapted to perform the steps of:

generating a stream of instructions for evaluation of a Boolean function in said design;

constructing a set of inputs for said Boolean function, input resources associated with members of said set of inputs being unidentified, and said inputs being outcome determinative of said Boolean function, said Boolean function further having an output resource;

selecting one of said members;

resolving an identity of said selected member;

excluding said selected member from said set of inputs;

removing all remaining members of said set of inputs that are no longer outcome determinative of said Boolean function; and

iterating said steps of selecting, resolving, excluding and removing until no more than one member remains in said set of inputs.

33. (Original) The verification system according to claim 32, wherein said test generator is further adapted to perform the steps of:

determining said output resource as a copy of said one member;

setting a flag that designates said output resource as being unidentified; and

including said output resource in a set of unidentified resources that is associated with said one member, a respective status flag being associated with each member of said set of unidentified resources that designates an unidentified status thereof.

34. (Original) The verification system according to claim 33, wherein said test generator is further adapted to perform the steps of:

associating a respective inversion flag with each of said input resources and said output resource that indicate an inversion status thereof; and

setting said inversion flag of said output resource to a negation of said inversion flag of one of said input resources that corresponds to said one member.

35.-43. (Cancelled)